

Form PTO-1449	U.S. Dept. of Commerce Patent & Trademark Office	Atty. Docket: 01-S-078	AUG 21 2002	Serial No. 10/037,967
List of Documents Cited by Applicant (Use several sheets if necessary)		Applicant: James CHOW et al.		
		Filing Date: December 21, 2001	Group: 2631	

U.S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

[illegible]

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

↕	AA2	"CMOS Flip-Flop Construction" at http://www.play-hookey.com/digital/cmos_d_flip-flop.html .
↓	AA3	Fiedler et al., "FP 15.1: A 1.0625Gbps Transceiver with 2x-Oversampling and Transmit Signal Pre-Emphasis", ISSCC97/Session 15/Serial Data Communications/Paper FP 15.1, pp. 186-187.
↓	AA4	Yang et al., "A 0.8- μ m CMOS 2.5 Gb/s Oversampling Receiver and Transmitter for Serial Links", IEEE Journal of Solid-State Circuits, Vol. 31, No. 12, December 1996, pp. 2015-2023.

Examiner:

Phenolphthalein

Date Considered:

3/17/05

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.